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Ronny Krashinsky, Christopher Batten, Mark Hampton, Steve Gerding, Brian Pharris, Jared Casper, Krste Asanovic

June 2004   **ISCA '04**: Proceedings of the 31st annual international symposium on Computer architecture
Publisher: ACM

Full text available: Pdf (317.13 KB)   Additional Information: full citation, abstract, references, cited by

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The vector-thread (VT) architectural paradigm unifies the vector and multithreaded compute models. The VT abstraction provides the programmer with a control processor and a vector of virtual processors (VPs). The control processor can use vector-fetch commands to ...

Also published in:
March 2004 **SIGARCH Computer Architecture News** Volume 32 Issue 2

2   Lx: a technology platform for customizable VLIW embedded processing

Paolo Faraboschi, Geoffrey Brown, Joseph A. Fisher, Giuseppe Desoli, Fred Homewood

June 2000   **ISCA '00**: Proceedings of the 27th annual international symposium on Computer architecture
Publisher: ACM

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Also published in:

May 2000 **SIGARCH Computer Architecture News** Volume 28 Issue 2

3 Mapping irregular applications to DIVA, a PIM-based data-intensive architecture

Mary Hall, Peter Kogge, Jeff Koller, Pedro Diniz, Jacqueline Chame, Jeff Draper, Jeff LaCoss, John Granacki, Jay Brockman, Apoorv Srivastava, William Athas, Vincent Freeh, Jaewook Shin, Joonseok Park

January 1999 **Supercomputing '99:** Proceedings of the 1999 ACM/IEEE conference on Supercomputing (CDROM)

Publisher: ACM

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